# LADOT Model 242-LAX

DC Isolator Unit
Operations Manual

# THIS MANUAL CONTAINS TECHNICAL INFORMATION FOR THE MODEL 242-LAX DC ISOLATOR.

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#### 1.1 GLOSSARY

A - Ampere

°C - Celsius

Component - Any electrical or electronic device

DC - Direct Current

Firmware - Program code embedded into a microcontroller unit

Hz - Hertz

IC - Integrated Circuit

Jumper - A means of connecting/disconnecting two or more conductive by soldering/desoldering a conductive wire, shunt, or by PCB post jumper

LED - Light Emitting Diode

LOGIC - Negative Logic Convention (Ground True) State

mA - milliAmpere

ms - millisecond

MCU - Micro Controller Unit or microcontroller unit

MOV - Metal Oxide Varistor

Opto-coupler – An integrated circuit that provides electrical isolation

PCB - Printed Circuit Board

RMS - Root-Mean-Square

s - second

Schmitt Circuit – a circuit that provides hysterisis in the threshold

SW - Switch

uF - microfarad

VAC - Voltage Alternating Current

VDC - Voltage Direct Current

#### 1.2 GENERAL DESCRIPTION

The Eberle Design Model 242-LAX is a dual channel DC Isolator unit designed to meet Caltrans specifications TEES March 2009. The isolator unit occupies one position of a 170 standard input file. The isolator unit card incorporates a double-sided 44 pin edge connector for the connection of power, input, and output signals. Each channel has individual front panel controls for testing the operational mode, and high intensity front panel LEDs which are used to indicate the output state. Outputs are optically-isolated solid-state transistors.

#### 1.3 GENERAL CHARACTERISTICS

Each channel of the Model 242-LAX provides input hysterisis and digital filtering to qualify the input signal. An input signal must meet the minimum pulse requirements specified in section 1.7.5 in order to produce a valid output. The output pulse width for both channels is set to 100 milliseconds minimum (jumper E3 installed).

Both the input and output circuits have been designed for maximum protection from electrical transients. The inputs have been designed to withstand the discharge of a 10 uF capacitor charged to +/- 1000 Vdc directly across the input pins, and a discharge of a 10 uF capacitor charged to +/- 2000 Vdc applied through a source impedance of 5 ohms across the input pins or to Equipment Ground. The outputs are protected by a transient clamp diode.

The Model 242-LAX handle assembly is made of GE Lexan<sup>™</sup> Type 121, which is a super durable polycarbonate resin. The design of this assembly strengthens and protects the whole PCB assembly much better than conventional metal face plates.

#### 1.4 INSTALLATION AND ADJUSTMENTS

Installation of the unit consists of plugging into the appropriate slot of the Input File and connecting the assigned inputs to the proper cabinet terminals. The edge connector is keyed to prevent incorrect installation. Following power-up, a front panel LED test will illuminate both OUT indicators for two seconds.

The output pulse width for both channels is set to 100 milliseconds minimum (jumper E3 installed).

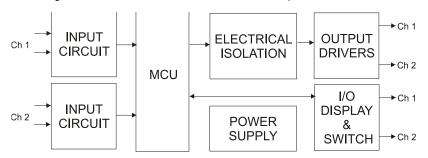
#### 1.5 THEORY OF OPERATION

Reference designators shown are for Channel 1. Reference designators for channel 2 are shown in parenthesis.

#### 1.5.1 SYSTEM DESCRIPTION

The sensor circuitry can be broken down into seven major blocks. Each "Input Circuit" block contains the electrical transient devices and the input biasing circuit. Although the Model 242-LAX has two DC inputs, a single digital processing section is used to process both inputs.

The microcontroller unit "MCU" Block provides the input voltage threshold and input pulse width filtering function on both channel 1 and channel 2 DC input circuits.



The MCU then controls the OUTPUT and DISPLAY blocks appropriately.

The microcontroller also reads the state of the TEST switches on the front panel. If the TEST switch is in the ON (locked) or MOM (momentary) position it will force the output to the asserted (True) state regardless of the input circuit state. The TEST switches are processed through the MCU for pulse width input and output requirements.

Valid output calls are made via optically isolated solid state transistors. Output calls are indicated on the front panel by means of high intensity LEDs labeled "OUT".

The Model 242-LAX operating voltage is generated by a high efficiency off-line switching power supply. The VDD supply for the microcontroller and display elements results from a post regulated 5 Vdc.

#### 1.5.2 INPUT CIRCUIT

Resistors R5, R7, R11, and R12 (R6, R8, R13, and R17) provide the input bias and voltage scaling circuit. The resulting voltage at U8.8 (U8.9) is then processed by the microcontroller U8. Metal Oxide Varistor RV4 (RV5) provides input electrical transient protection.

#### 1.5.3 MCU CIRCUIT

The microcontroller U8 processes the input voltages to perform the voltage threshold and pulse width filtering functions. All signal processing is performed in the digital domain and

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controlled by firmware embedded in the microcontroller. This unit does not rely on analog delay, pulse, or comparator circuits for processing the input signals.

#### 1.5.4 OUTPUT CIRCUIT

The output driver Q2 (Q1) is isolated from both the AC Mains and the internal GND reference of the Model 242-LAX by opto-couplers U6 (U2). CR1 (CR2) provides electrical transient protection for the output driver Q2 (Q1).

#### 1.5.5 DISPLAY AND SWITCH CIRCUIT

The LED indicator DS1 (DS2) for the channel output is driven directly from the microcontroller U8. The input TEST switch SW2 (SW3) connects the analog input of MPU U8 to ground when activated.

#### 1.5.6 POWER SUPPLY CIRCUIT

The main power supply is a fully isolated switching design. MOV RV7 provides electrical transient protection. The AC Mains voltage is rectified and charges C3 to a nominal 170 Vdc. Controller U1 drives transformer T1 at approximately 144 KHz to produce an isolated DC voltage at C15, C16, and C17. Inductor L1 and C14 filter the high frequency switching noise. The resulting voltage at VCC is regulated to 19.0 +/- 1 Vdc. The opto-coupler U4 and reference U5 provide the closed loop feedback to the power supply controller U1 for regulation. Regulator VR1 regulates VCC down to 5.0 Vdc for the microcontroller U8.

#### 1.6 MAINTENANCE

The Model 242-LAX requires no adjustments or preventive maintenance.

#### 1.6.1 TROUBLE ANALYSIS

The following list should be used to trouble-shoot the Model 242-LAX installation. If the Model 242-LAX unit itself is suspect, see Section 1.6.2 for a complete internal testing sequence.

- Neither channel responds to DC inputs
  - a. Power supply fault

The Model 242-LAX requires a 115 Vac nominal supply. The unit will operate at voltages as low as 80 Vac, however, an AC Mains voltage below this may result in the unit entering a reset state. In this case, the unit will appear to be non-functional.

- b. Channel does not detect all inputs
  - Input voltage or pulse width does not meet the requirements of section 1.7.4 or 1.7.5.

Verify that the DC input voltage level is less than the DC Inputs True specification. Verify that the DC input pulse width is greater than the Input Pulse Width Accept specification.

#### 1.6.2 TROUBLE SHOOTING SEQUENCE

Apply 115 Vac nominal mains power to AC+ pin J1-N referenced to AC- pin J1-M. Connect a jumper circuit to the DC inputs J1-D (J1-J) and J1-E (J1-K) to simulate the closure of the input contacts. The following signal measurements are referenced to test point "GND".

NOTE: internal test point "GND" is isolated from AC-. Care should be exercised in probing internal test points.

a. Input Bias Power Supply

Voltage at test point V\_UNF should be  $19 \pm 1$  Vdc.

Possible component faults are: controller U1, diodes CR10 and CR14, transformer T1, inductor L1, or opto-coupler U4.

Regulated Power Supply

Voltage at test point VDD should be  $5 \pm 0.2$  Vdc.

Possible component faults are: voltage regulator VR1.

#### c. Microcontroller

Waveform at pin 5 of P1 (or U8.18) should be a 2.5 us high to low pulse every 1 millisecond.

Possible components at fault are: microcontroller U8.

#### d. Output Circuit

Output signals are processed by the microcontroller U8 and appear at U8.6 (U8.5) and on the display LEDs but are not appearing at the output pins.

Possible components at fault are: opto-coupler U6 (U2), output transistor Q2 (Q1).

#### 1.7 SPECIFICATIONS

#### 1.7.1 CONSTRUCTION

Printed circuit boards are double sided 2 oz. (56.70 gm.) copper with plated through holes. Circuit boards are coated for environmental protection.

#### 1.7.2 MECHANICAL

Height	4.50 inches
Width	1.2 inches
Depth (excluding handle)	6.875 inches

#### 1.7.3 ENVIRONMENTAL

Storage Temperature Range	-45 to +85 °C
Operating Temperature Range	-34 to +74 °C
Humidity Range (non-condensing)0 to	

#### 1.7.4 ELECTRICAL

AC Supply Voltage Minimum	80 Vac
AC Supply Voltage Maximum	135 Vac
AC Supply Power Maximum	2.5 Watts
DC Inputs	

True (low) ...... less than 8 Vdc False (high) ...... greater than 12 Vdc

#### Optically Isolated Solid State Outputs

True (low, 50 mA)	less than 1.5 vac
False (high)	greater than 16 Vdc
Maximum Leakage Current (high)	less than 1 uA
Maximum Current	50 mA
Collector Voltage Maximum	50 Vdc

#### **1.7.5 TIMING**

Input Pulse Width Reject	less than 5 ms
Input Pulse Width Accept	greater than 25 ms

#### 1.7.6 CONNECTIONS

Edge Connector mates with connector type Cinch 50-44A-30

PIN	FUNCTION	
D	Input CH 1	
E	Input CH 1 Common	
F	CH 1 Output Collector	

PIN	FUNCTION	
Н	CH 1 Output Emitter	
J	Input CH 2	
K	Input CH 2 Common	
L	Equipment Ground	
M	AC -	
N	AC +	
W	CH 2 Output Collector	
X	CH 2 Output Emitter	

#### 1.8 SCHEMATIC AND PARTS LIST

Item	Tem   EDI Part Number   Qty   Description   Reference			
1		1	(NO COMPONENT)	   j
2		6	(NO COMPONENT)	GND HV+ HV- VCC VDD V UNF
3		2	(-10 00111 011111 )	M1-2
4	İ	ī	PIC Programming Port	P1
4 5	215-5610-S	4		R5-8
6	220-5600-S	2		R3-4
7	251-1052-S	3		R11 R13 R30
8	251-1211-S	ĺ	RESISTOR, 1/8W, 1.21K, 1%, 1206 surface mount	R26
9	251-1961-S	3	RESISTOR, 1/8W, 1.96K, 1%, 1206 surface mount	R12 R17 R31
10	251-8061-S	lí	RESISTOR, 1/8W, 8.06K, 1%, 1206 surface mount	R25
11	255-0000-S	8	RESISTOR, 1/8W, 0 OHMS, 5%, 1206 surface mount	E2-4 E6 E8 E10-12
12	255-1020-S	3	RESISTOR, 1/8W, 1K, 5%, 1206 surface mount	R21 R24 R32
13	255-1030-S	2	RESISTOR, 1/8W, 10K, 5%, 1206 surface mount	R14-15
14	255-1040-S	2		R22 R33
15	255-4310-S	ĺ	RESISTOR, 1/8W, 430 Ohm, 5%, 1206 surface mount	R27
16	255-4720-S	4	RESISTOR, 1/8W, 4.7K, 5%, 1206 surface mount	R1-2 R9 R29
17	255-4730-S	2	RESISTOR, 1/8W, 47K, 5%, 1206 surface mount	R19-20
18	255-5110-S	2		R16 R18
19		2	RESISTOR, 1/8W, 620 Ohm, 5%, 1206 surface mount	R35 R37
20		1	CAPACITOR, ELECTROLYTIC, 100uf, 35V, 20%, SMT,6.3	
20	300-1070-0333	1 +	x 8	
21	300-3360-250R	1	CAPACITOR, ELECT, 33uF, 250WV, 20 %, RDL	lc3
22	300-3370-035S	3	CAPACITOR, ELECTROLYTIC, 330uF, 35V, LOW ESR,	C14 C16-17
		1	20%, SMT	
23	310-1060-006S	1	CAPACITOR, TANTALUM, 10UF, 6.3V, 20%, 1206 CHIP	C18
24	320-1020-050S	2	CAPACITOR, CER.MULT, 0.001uF, 50V, 10%, 1206 CHIP	
25	320-1030-100S	2	CAPACITOR, CER.MULT, 0.01uF, 100V, 10%, 1206 CHIP	C8-9
26	320-1040-050S	5	CAPACITOR, CER.MULT, 0.1uF, 50V, 10%, 1206 CHIP	C1-2 C15 C19-20
27		ĺĭ	CAPACITOR, CER.DISC, 100pF, 500V, 10%, RDL	C12
	325-4700-500R	1 1	CAPACITOR, CER.DISC, 47pF, 500V, 10%, RDL	c11
29	335-1040-630R	2	CAPACITOR, 0.1UF, 630V, 10%, METALIZED FILM, 15mm	C5-6
30	410-0053-S	2	DIODE, TRANS. SUPR., SMA5.0A, 5V,SMA	CR9 CR11
31	410-0140-S	ī	DIODE, SCHOTTKY, MBRS140T3, 40V,1A, SMB	CR14
32	410-0160-S	ī		CR10
33	410-1526-S	ī		CR13
34	410-4005-S	4		CR4-7
35	410-4148-S	6		CR15-20
36	410-4755-S	2	DIODE ZENER CMZ5941B 1 OW 5% 43V CMA	CR1-2
37		3		U2 U4 U6
38		2	DISPLAY, LED MODULE, DUAL, RA, HIGH BRIGHTNESS,	DS1-2
		ĺ	RED ONLY	1001 2
39	430-0006-S	2		01-2
40	430-0006-S 440-0030 440-0150	2	REGULATOR, METAL OXIDE VARISTOR	RV4-5
41	440-0150	1 1		RV7
1	1 1 1 0 0 1 2 3 3	1 -	independent in the state of the	1247

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42	U5 U1 VR1 U8 SW2-3 CVR1 T1 L1 SW1
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