Inte	ersection:							Date:							
1-2 2-3	1-3 2-4	1-4	1-5 2-6	1-6 2-7	1-7 2-8	1-8 2-9	1-9 2-10	1-10 2-11	1-11	1-12 2-13	1-13 2-14	1-14 2-15	1-15	1-16	
3-4	3-5	3-6	3-7	3-8	3-9	3-10	3-11	3-12	3-13	3-14	3-15	3-16			
4-5	4-6	4-7	4-8	4-9	4-10	4-11	4-12	4-13	4-14	4-15	4-16				
5-6	5-7	5-8	5-9	5-10	5-11	5-12	5-13	5-14	5-15	5-16					
6-7	6-8	6-9	6-10	6-11	6-12	6-13	6-14	6-15	6-16						
7-8	7-9	7-10	7-11	7-12	7-13	7-14	7-15	7-16							
8-9	8-10	8-11	8-12	8-13	8-14	8-15	8-16								
9-10	9-11	9-12	9-13	9-14	9-15	9-16			Ped	Phase:					
10-11	10-12	10-13	10-14	10-15	10-16					_					
11-12	11-13	11-14	11-15	11-16	]				FYA	Mode:					
12-13 13-14	12-14 13-15	12-15 13-16	12-16	]					Overlap F	)hacacı					
14-15	14-16	13-16	_						Overlap r						
15-16	14 10	J													
					NE	M	Δ								
Permitted															
Cor	nflict									.					
	lunit Onti	one								<u> </u>	<u> </u>				
Unit Options Permissive Channel Pairs															
Flashing Yellow Arrow															
								(-2-							
											$ op \lceil$				
										ĺ	   <b> -</b>				
Check b	w.									'[					
CHECK	.1.	•—					Note: This	exmple is f	or illustrat						
		:					Note: This exmple is for illustrative purposes only. The permissive programming card is dependent on the actual								
				intersection geometry, cabinet wiring, and controller programming											
			IP Address:												